

Serial No. 10/042594

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Inventor(s): Matthias Berger
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MAR 18 2004

Technology Center 2600

Case: 5-5-4-1-3-3

Serial No.: 10/042594

Filing Date: January 9, 2002

Examiner:

Group Art Unit: 2816

Title: Method And Apparatus For Correcting The Phase Of A Clock In A Data Receiver

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(b)

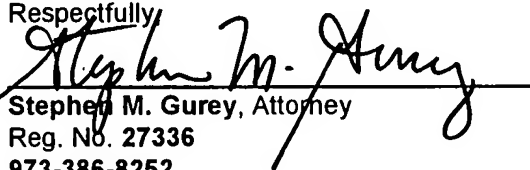
In accordance with 37 CFR 1.97(b), the enclosed Information Disclosure Statement, with attached reference(s), is submitted for consideration in the above-identified application.

Copies of the listed documents are enclosed.

NO FEE IS REQUIRED

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Lucent Technologies Deposit Account No. 12-2325 as required to correct the error.

Respectfully,


Stephen M. Gurey, Attorney
Reg. No. 27336
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Date: March 15, 2004

Docket Administrator (Room 3J-219)
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop DD, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, on March 15, 2004.


Rose E. Ewald

Case Name:	M. Berger 5-5-4-1-3-3
Serial No.	10/042594
Applicant:	M. Berger, et al.
Filing Date:	January 9, 2002
Group:	2816

[illegible]

FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation

	AA	J. D. H. Alexander, "Clock Recovery From Random Binary Signals", <i>Electronic Letters</i> , Volume 11, (10/1975), pp 541-542.

***Note First Page ONLY Header/Footer. Subsequent pages must ONLY have page # layout as header

EXAMINER	DATE CONSIDERED
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PT968-1.97Form